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| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
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10/621,771

07/17/2003

Makoto Yoshino

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08/12/2004

TEXAS INSTRUMENTS INCORPORATED

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EXAMINER

CLARK, SHEILA V

ART UNIT

PAPER NUMBER

2815

DATE MAILED: 08/12/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

| | | | |
|------------------------------|-------------------------------|--------------------------------|--|
| Office Action Summary | Application No. 10/621,771 | Applicant(s) YOSHINO ET AL. | |
| | Examiner S. V. Clark | Art Unit 2815 | |

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 7-17-2003 (Prelim Amd).
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-4 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-4 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

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Claim 2 is rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the written description requirement. The claim(s) contains subject matter which was not described in the specification in such a way as to reasonably convey to one skilled in the relevant art that the inventor(s), at the time the application was filed, had possession of the claimed invention.

The definition of the equation recited in claim 2 fails to be adequately defined and therefore described in the specification. On page 7, next to the last paragraph pitch p is said to be determined but a relationship with pitch L and the equation is presented but there appears to be discussion nor presentation of any quality relationship between pitch p and L though the equation appears to present such. Page 8 though which continues to discuss the relationship also fails to discuss any equality of the relationship. The equation therefore lacks understanding and therefore lacks clarity.

Claims 3 and 4 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

It is not clear what is meant by "for plating-electricity-supply-use" conductor pattern. Since this phrase is not a well known in this art and has not been defined, in the absence of any specific definition of function the phrase has been taken to mean a "conductor pattern". The words in the phrase also do not appear to fit together nor do they describe said conductor.

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.

Claims 1-2 are rejected under 35 U.S.C. 102(a) as being anticipated by Kondou.

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Kondou shows in for example figure 2 an insulation film in the form of a carrier tape 1 for carrying a semiconductor chip 4. Two opposing rows of sprocket holes 2 are shown along two edges of the film and having a pitch L. A plurality of two dimensional through holes 3 are shown (plurality in figure in 7) formed two dimensionally at a pitch p between said sprocket holes. Said through holes be used as through holes for the insulation package.

As values for L, P, M nor P appear to be defined in claim 2 it is inherent that Kondou inherently possesses values that may satisfy the equation.

Claims 1-4 are rejected under 35 U.S.C. 102(a) as being anticipated by Distefano

Distefano shows in for example figures 8A, 8H and 8I an insulation film 101 for carrying a semiconductor chip. Two opposing rows of sprocket holes 105 are shown along two edges of the film and having a pitch L. A plurality of two dimensional through holes 104 are shown formed two dimensionally at a pitch p between said sprocket holes. Said through holes be used as through holes for the insulation package.

As values for L, P, M nor P appear to be defined in claim 2 it is inherent that Distefano inherently possesses values that may satisfy the equation.

A plurality of circuit patterns 108 are formed on said insulation film and a conductor pattern 121 is shown electrically connected to said patterns. A main line terminal 122 is shown in figure 8I surrounding said patterns and sub line contact 123 is shown electrically connected to the main line.

Claims 1-4 are rejected under 35 U.S.C. 102(a) as being anticipated by Amagai

Amagai shows in for example figures 3 and 4 an insulation film 3 for carrying a semiconductor chip. Two opposing rows of sprocket holes unlabelled are shown along two edges of the film and having a pitch L. A plurality of two dimensional through holes 6 in figure 4 and are shown (plurality in figure in 7) formed two dimensionally at a pitch p between said sprocket holes. Said through holes be used as through holes for the insulation package.

As values for L, P, M nor P appear to be defined in claim 2 it is inherent that Aamgi inherently possesses values that may satisfy the equation.

A plurality of circuit patterns 7a are formed on said insulation film and a conductor pattern 7b, 7c is shown electrically connected to said patterns. A main line 7c is shown


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in figure 3 surrounding said patterns and sub line contact 7b is shown electrically connected the patterns to the main line.

Claims 1-4 are rejected.

Hahsimoto and Cho are cited to show dielectrics having sprocket holes.

Any inquiry concerning this communication should be directed to S. V. Clark at telephone number (571) 272-1725.



S. V. Clark
Primary Examiner
Art Unit 2815

August 5, 2004